

## REMARKS

This Amendment is submitted in response to the Examiner's Action mailed October 3, 2003, with a shortened statutory period of three months set to expire January 3, 2004. Claims 1-19 are currently pending. With this amendment, claims 1, 7-9, 13, and 19 have been amended.

Applicants have amended the specification to correct some typographical errors. Specifically, Applicants have amended the text so that the reference numbers in the text are consistent with the reference numbers depicted in Figure 3. No new matter has been added.

Applicants' claims now describe a different contiguous range of virtual address pages being associated with each one of the logical partitions. A noncontiguous group of page frames of real memory addresses are then assigned to each one of the contiguous range of virtual address pages. In this manner, each of the logical partitions is assigned a different noncontiguous group of page frames of real memory addresses.

Applicants' Figure 3 and the accompanying description describe the physical resources 360 being divided into fixed size page frames. See specification page 13, lines 15-16. Also depicted by Figure 3 are multiple page frame tables of virtual addresses. Each page frame table is associated with one of the partitions. The pages of a partition's page frame table are not mapped to a contiguous range of the physical page frames. “[T]here is no requirement that the pages used by the OSs 302-308 be stored consecutively in the physical resources 360. Furthermore, there is no requirement that a page frame 361-371 in physical resources 360 be allocated such that each of the page frames 361-371 allocated to a particular partition to be grouped consecutively.” See specification page 13, line 26, to page 14, line 2.

The Examiner rejected claims 1-22 under 35 U.S.C. § 102(e) as being anticipated by U.S. Patent Application Publication US 2003/0009648 by *Doing*. This rejection, as it might be applied to the claims as amended, is respectfully traversed.

Applicants describe each of the logical partitions being assigned a different noncontiguous group of page frames of real memory addresses. *Doing* does not describe each logical partition being assigned to a noncontiguous range of real memory addresses.

*Doing* teaches a processor that sets the high order physical address bits to a specific pattern unique to the partition that is running. *Doing* constrains the real addresses generated by the processor for a partition to be within a contiguous group of addresses, as defined by the high order bit pattern. Applicants' claims describe a method, system, and product whereby a partition is not constrained to a specific range of contiguous page frames of real memory addresses.

Therefore, it is respectfully urged that the subject application is patentable over the prior art. The examiner is invited to call the undersigned at the below-listed telephone number if in the opinion of the examiner such a telephone conference would expedite or aid the prosecution and examination of this application.

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Respectfully submitted,



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